

**FIG. 2**

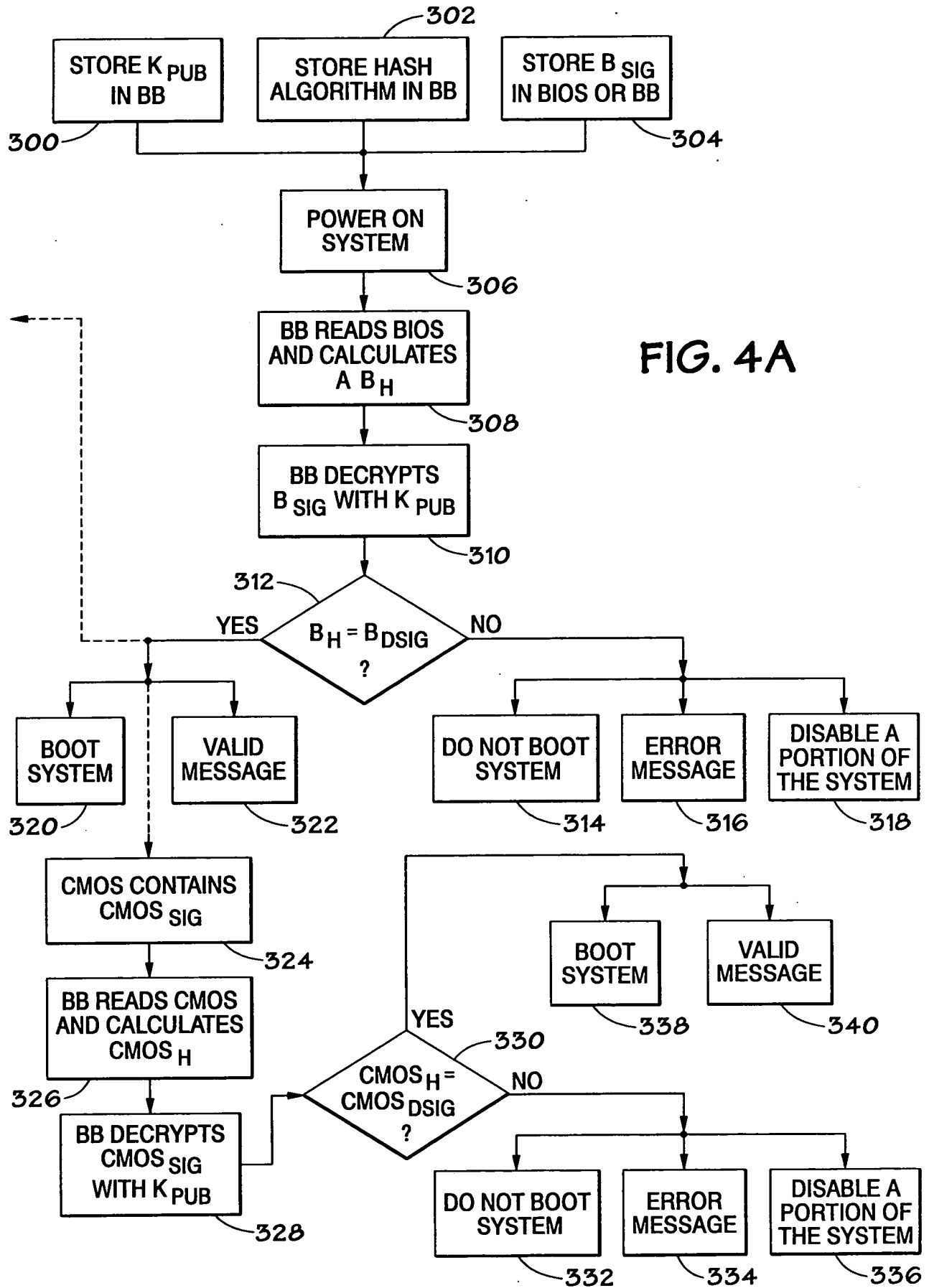
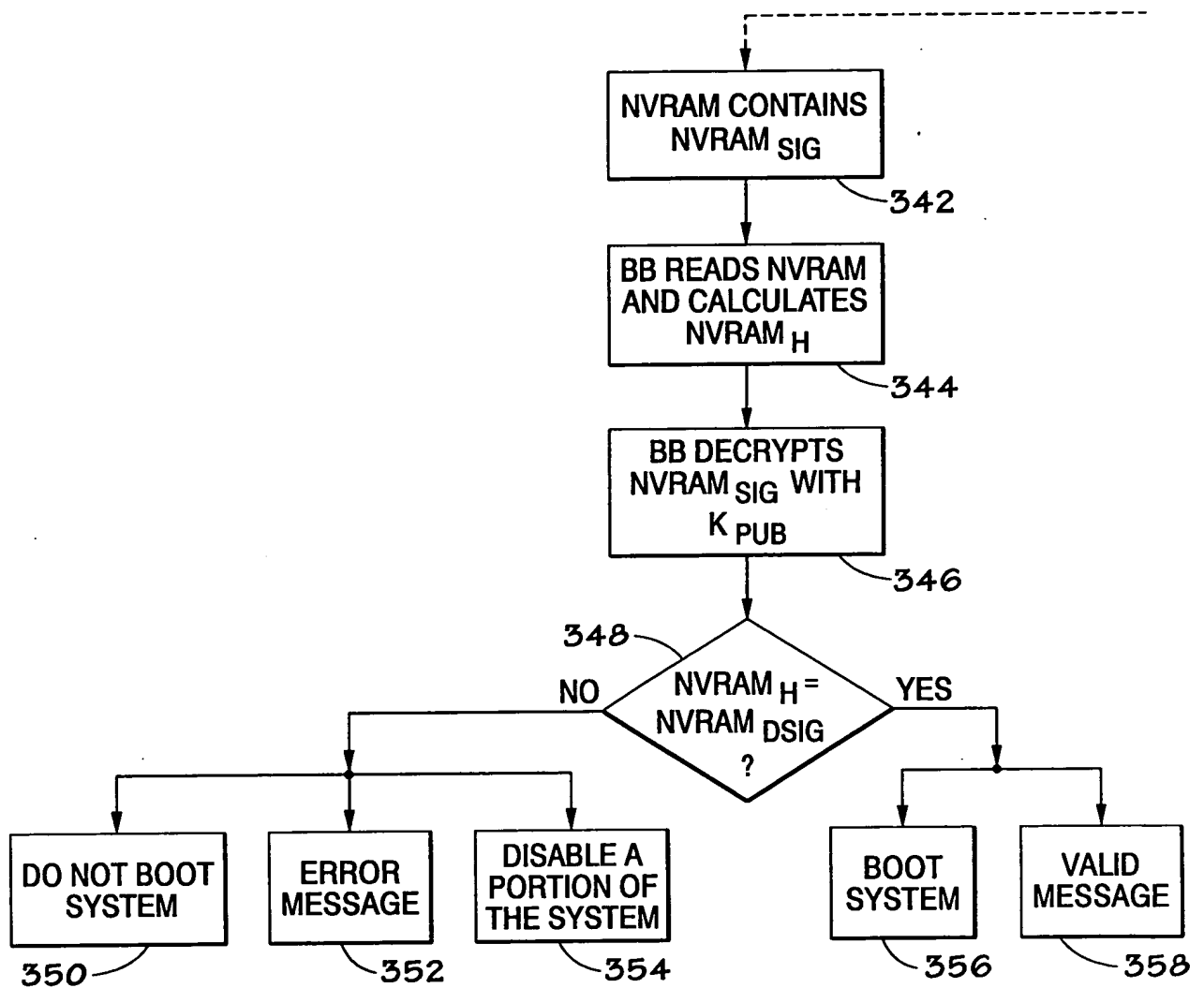


FIG. 4B



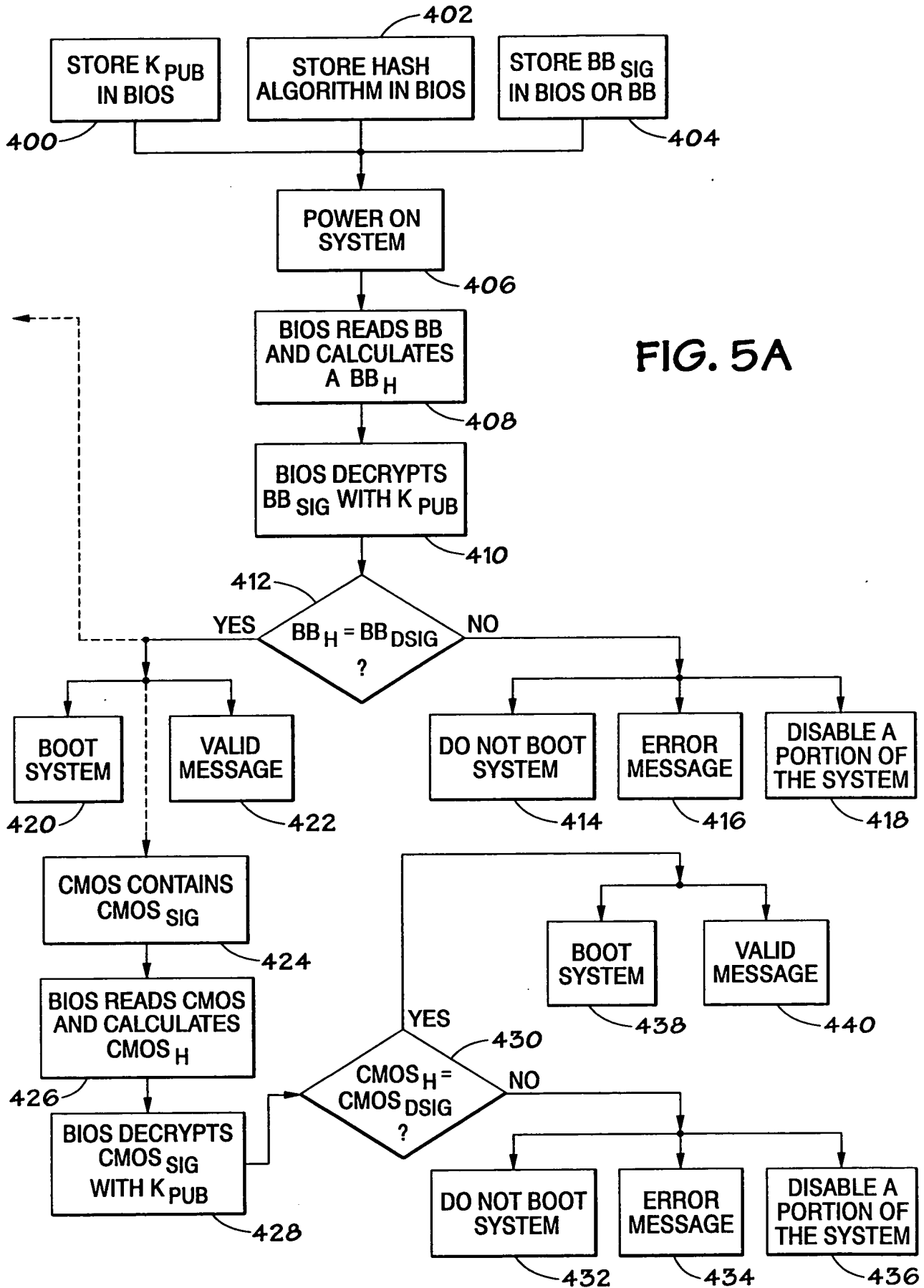


FIG. 5A

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FIG. 5B

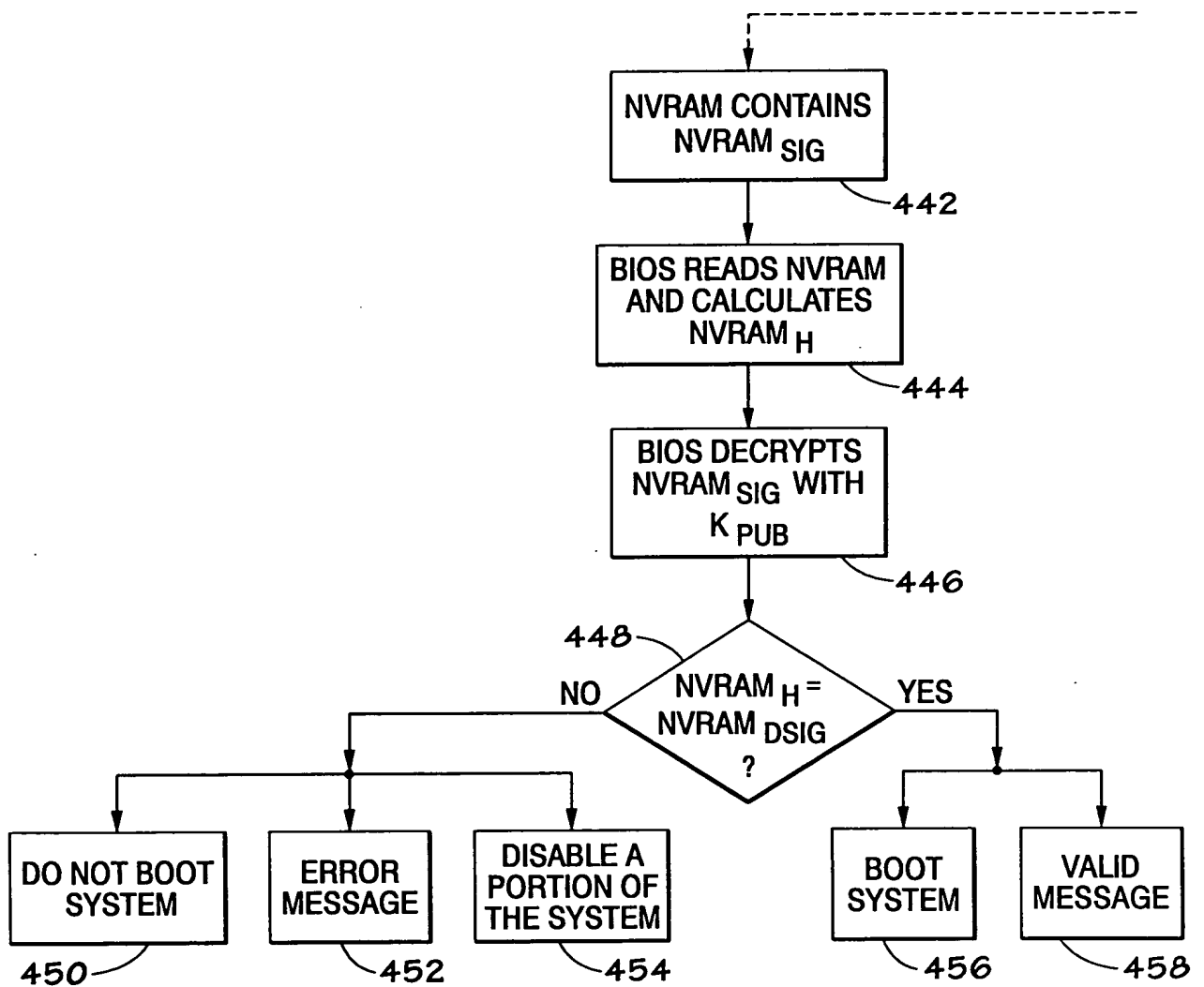


FIG. 6A

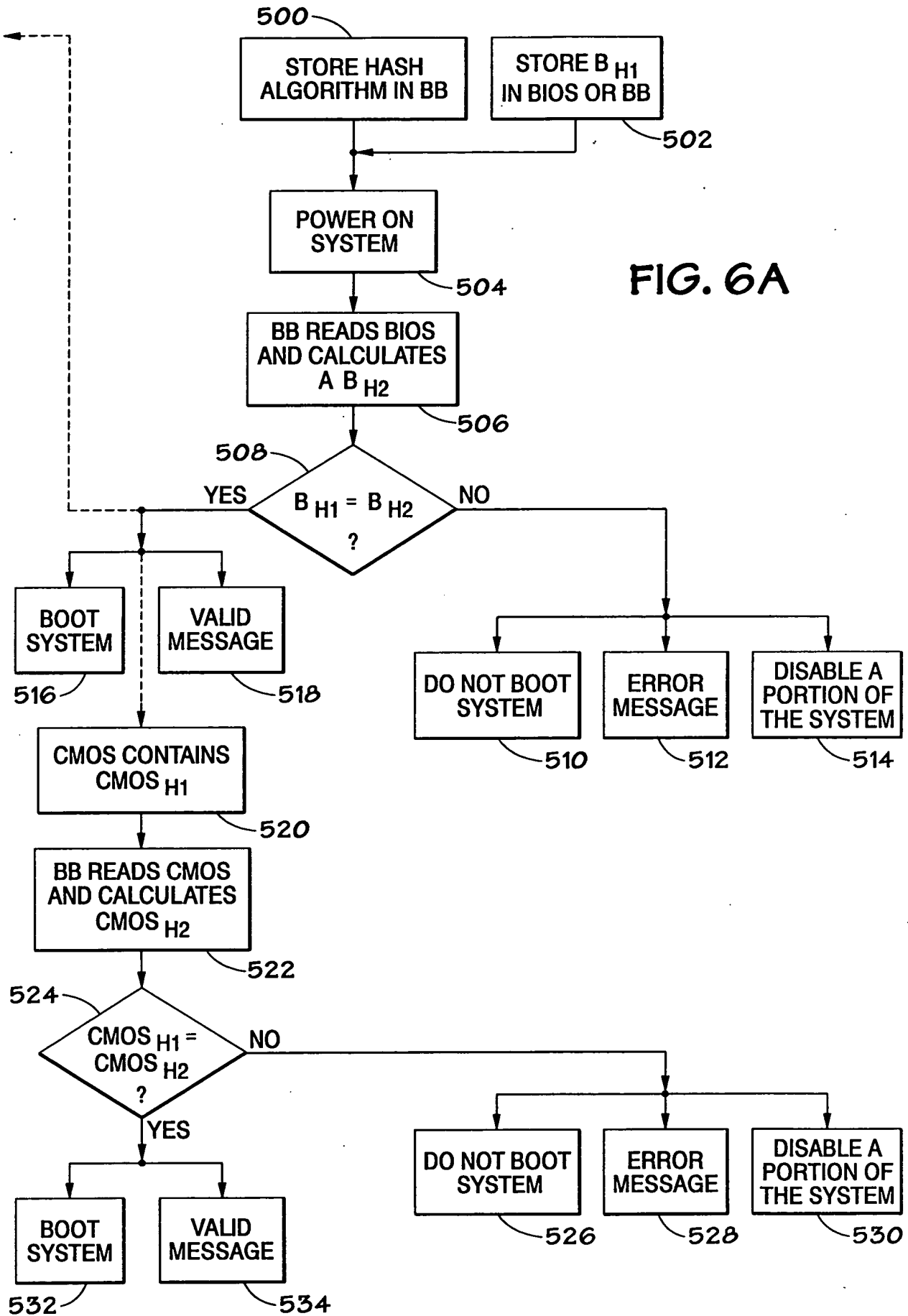


FIG. 6B

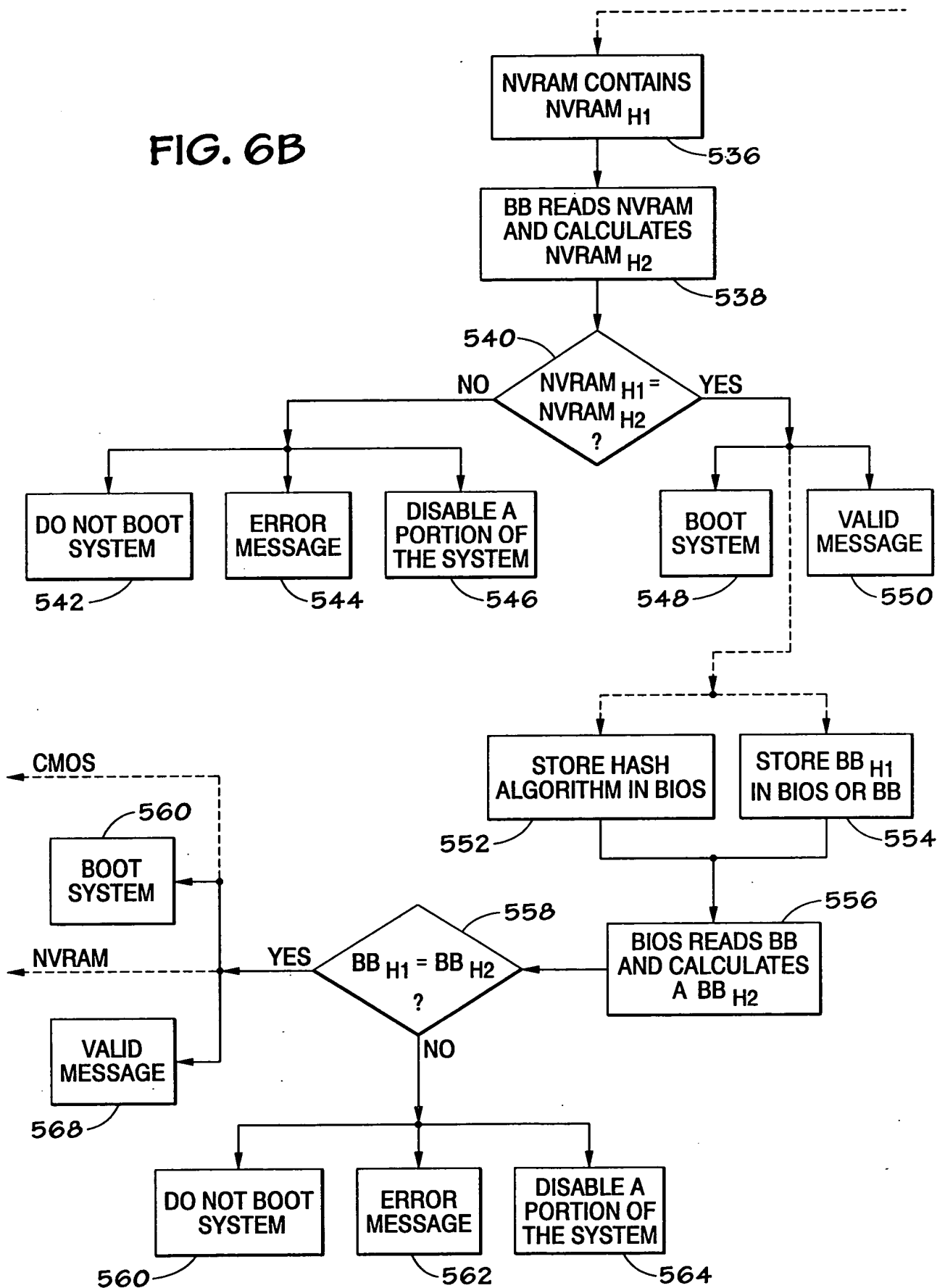
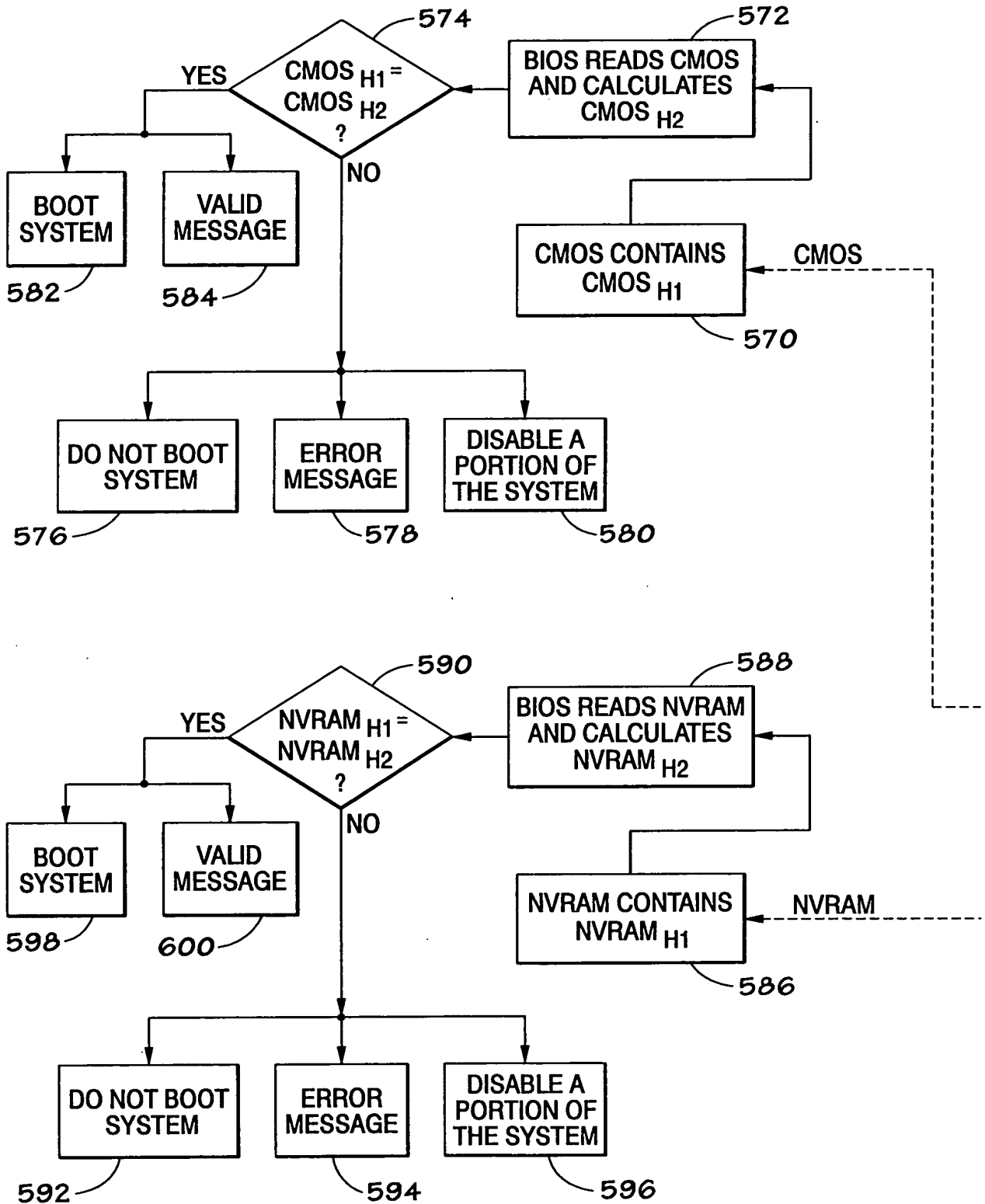




FIG. 6C



SYMBOL	DESCRIPTION
K <sub>PUB</sub>	PUBLIC KEY
B <sub>H</sub>	BIOS HASH
B <sub>SIG</sub>	BIOS SIGNATURE( BIOS ENCRYPTED HASH)
B <sub>DSIG</sub>	BIOS SIGNATURE DECRYPTED
B <sub>H1</sub>	BIOS HASH THAT SHOULD BE PRESENT
B <sub>H2</sub>	BIOS HASH THAT IS PRESENT
BB	BOOT BLOCK
BB <sub>H</sub>	BOOT BLOCK HASH
BB <sub>SIG</sub>	BOOT BLOCK SIGNATURE (BOOT BLOCK ENCRYPTED HASH)
BB <sub>DSIG</sub>	BOOT BLOCK SIGNATURE DECRYPTED
BB <sub>H1</sub>	BOOT BLOCK HASH THAT SHOULD BE PRESENT
BB <sub>H2</sub>	BOOT BLOCK HASH THAT IS PRESENT
CMOS <sub>H</sub>	CMOS HASH
CMOS <sub>SIG</sub>	CMOS SIGNATURE (CMOS ENCRYPTED HASH)
CMOS <sub>DSIG</sub>	CMOS SIGNATURE DECRYPTED
CMOS <sub>H1</sub>	CMOS HASH THAT SHOULD BE PRESENT
CMOS <sub>H2</sub>	CMOS HASH THAT IS PRESENT
NVRAM <sub>H</sub>	NVRAM HASH
NVRAM <sub>SIG</sub>	NVRAM SIGNATURE (NVRAM ENCRYPTED HASH)
NVRAM <sub>DSIG</sub>	NVRAM SIGNATURE DECRYPTED
NVRAM <sub>H1</sub>	NVRAM HASH THAT SHOULD BE PRESENT
NVRAM <sub>H2</sub>	NVRAM HASH THAT IS PRESENT

**FIG. 7**